

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	PROCESS FOR PLANARIZING ARRAY TOP OXIDE IN VERTICAL MOSFET DRAM ARRAYS		
Application Number :			
Date :			
First Named Applicant:		Sheng-Wei Yang	
Confirmation Number:			
Attorney Docket Number:		NTCP0008USA	
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Submitted By:		Elec. Sign.	
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Documents being submitted:	Files
us-assignment	NTCP0008-usassn.xml us-assignment.xsl us-assignment.dtd NTCP0008ASS1.tif NTCP0008ASS2.tif
us-request	NTCP0008-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	NTCP0008-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	NTCP0008-usdecl.xml us-declaration.dtd us-declaration.xsl
us-power-of-attorney-grant	NTCP0008-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-declaration application-body	NTCP0008DEC2.tif NTCP0008-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Ntcp0008usa-01.tif Ntcp0008usa-02.tif Ntcp0008usa-03.tif

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	Ntcp0008usa-09.tif
	Ntcp0008usa-10.tif
us-declaration	NTCP0008DEC1.tif
Comments	